

Remarks/Arguments

Claims 1-14 are pending. Claims 4-9 and 12 have been indicated to be allowable over the cited references. Claims 1 and 10 have been amended to more clearly and distinctly claim the subject matter that applicants regard as their invention. Support for the amendment is provided in, for example, page 2, lines 21-23 of the specification. No new matter is believed to be added by the present amendment.

Rejection of claims 1, 2, and 10 under 35 USC 102(e) as being anticipated by Pollman (US Pat No 5809538)

Applicants submit that for at least the reasons discussed below present claims 1, 2 and 10 are not anticipated under 35 USC 102(e) by Pollman.

Present claim 1 recites " ... means for receiving from a peripheral device, interconnected by a digital bus, bit-mapped data representative of an on-screen display **generated and controlled** by said peripheral device... (emphasis added)" Applicants submit that nowhere does Pollman disclose or suggest this limitation.

The Office action alleges that the arbitor 100 corresponds to the means for receiving and the DRAM 204 corresponds to the recited peripheral device. Applicants respectfully disagree and submit that DRAM 204 does not **generate and control** the on-screen display as recited in amended claim 1.

Pollman discloses a memory control and management system for efficiently controlling access to a memory among several client processes. In particular, the system includes terminal 610 for receiving a digital television signal that includes video, audio and data services, which may include graphical OSDs (col. 1, lines 21-27). In that regard, Pollman states "The OSD data is generated locally in a set-top box **according to programming data in the received television signal**. Predefined display templates and other formats are employed as a backdrop for the program scheduling data. (emphasis added)" (col. 1, lines 34-38)

In such a system, the data necessary for generating the graphical OSD is transmitted in a multiplexed datastream with the video and audio packets. The system filters the packetized datastream to acquire the OSD information, stores

that information in the DRAM, and later retrieves the data in the DRAM for processing by the OSD processor. That is, the DRAM merely stores the filtered data packets and later provides them to an OSD processor, which generates the OSD data that is applied to a display. The **DRAM itself does not generate and control the on-screen display**, instead, the on-screen display is generated and controlled by data transmitted with the digital television signals. Further, Pollman mentions the advantages of enabling the generation and control of OSDs using data transmitted with the digital television signals (col. 1, lines 18-34), but does not provide and teaching or suggestion regarding why it may be desirable to provide an on-screen display that is generated and controlled by the DRAM.

For at least the reasons discussed above, applicants submit that Pollman fails to disclose each and every limitation of amended claim 1, and as such, amended claim 1, and claim 2, which depends therefrom, are not anticipated by Pollman.

Claim 10 recites a similar limitation in method form. Thus, applicants submit that claim 10 is not anticipated by Pollman for at least the same reasons as those applied to amended claim 1.

Rejection of claim 3 under 35 USC 103(a) as being unpatentable over Pollman (US Pat No 5809538)

Pollman is further cited as teaching updating the OSD on any part of the television screen. However, applicants submit that the alleged further teachings of Pollman fail to cure the defect of Pollman as applied to amended claim 1. As such, applicants submit that claim 3, which depends from amended claim 1, is patentably distinguishable over the further cited teachings of Pollman.

Rejection of claims 11, 13 and 14 under 35 USC 103(a) as being unpatentable over Pollman (US Pat No 5809538) in view of Iwamura (US Pat No 5883621)

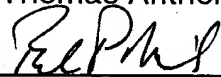
Iwamura is cited as teaching the use of the IEEE 1394 bus for communication OSD data with various A/V devices, using first and seconds modes of transfer, including asynchronous and isochronous modes. However, applicants submit that the alleged teachings of Iwamura still fail to cure the defect of Pollman as applied to amended claim 10. As such, applicants submit that claims 11, 13

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Internal Docket No. RCA 88,863
Customer No. 24498

and 14, which depends from amended claim 10, are patentably distinguishable over the suggested combination of Pollman and Iwamura.

Having fully addressed the Examiner's rejections it is believed that, in view of the preceding amendments and remarks, this application stands in condition for allowance. Accordingly then, reconsideration and allowance are respectfully solicited. If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant's attorney at (609) 734-6815, so that a mutually convenient date and time for a telephonic interview may be scheduled.

Respectfully submitted,
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Date: December 21, 2005

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